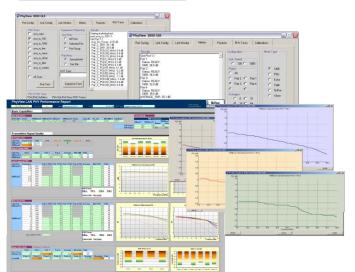


PVA-3000

PhyView[™] Analyzer
Gigabit & Fast Ethernet Transceiver Analysis



☐ Compact 2-Port PVA-3002 Format Available

Product Overview



Key Features

Comprehensive 10/100/1000 PHY Testing Made Easy
Just PlugRunEvaluate
Easier, Faster, and More Informative Alternative to Packet Testing
Answer "What's Wrong, How Wrong, and Where?"
Automatically Examine All PHY Performance Margins
Transmitted Signal Integrity
Receiver Performance Under Stress
Physical Interface Characteristics
Automatically Sequence Up To 24 DUT Ports per PVA-3000 Chassis
That's 96 Gigabit Pairs and 48 10/100BaseT Pairs!
No Scopes, Fixtures, Probes, Generators, Test Modes, or Cable Spools!
Test Any LAN Interface, Anywhere
Innovative New Measurements on Transmitted Signals
Versatile, Programmable Impairments for LAN Receiver Testing
Automated DC Unbalance Analysis When Combined with PSA-3102 Blades
Sifos PSA-3000 and PSA-1200 Chassis Compatible
Externally Accessible Impairments – Combine with Packet Analyzers

real Power from Sifos

Multi-Port PHY
Transmitter and
Receiver Tests
WITHOUT
Scopes, Probes,
Fixtures,
Generators, Test
Modes, and
Packet
Analyzers!

Comprehensive
Physical Layer
Test Reports on
Multiple LAN
Ports at the
Press of a Button

Find Defects and Weaknesses Invisible to Packet Tests

Innovative
Measurements
and Impairments
Including
Magnetic DC
Unbalance
Coverage

Overview

The PVA-3000 PhyView Analyzer is designed to bridge the wide coverage gap between comprehensive Ethernet twisted pair PHY compliance testing and ordinary packet verification testing. The PVA-3000 introduces an innovative multi-port capability *dedicated* to 10/100/1000BaseT physical layer characterization under controlled impairments including line loss, ingress noise, jittered or offset timing, and even Power-over-Ethernet. The PVA-3000 tests Ethernet switches and routers, discrete LAN interfaces, link transmission components, and network service integrity at any DTE interface.

Why Test Ethernet PHY's?

Conformance to all IEEE 802.3 specifications at the physical layer assures that a LAN port will successfully interoperate with other specification compliant equipment under all possible link configurations and conditions of connection impairment. Exhaustive physical layer testing also exposes and identifies any "weak links" that can adversely affect link performance. Physical layer testing is essential for qualifying new components including PHY silicon, magnetics, connectors, and physical layouts.

PHY Testing versus Ethernet Packet Testing

Ethernet PHY testing, as typically specified by IEEE 802.3 specifications, is both expensive and time consuming. It requires expensive test equipment and a high degree of expertise to perform. Most measurements are done pair-by-pair with considerable manual intervention. The types of measurements specified often defy automation and test coverage typically favors transmitter testing over receiver testing despite the equal role both elements play in enabling successful link-ups.

Packet transmission testing is widely available and has evolved as the convenient substitute to physical layer evaluation. In fact, many consider it an "equivalent" to physical layer evaluation. As a substitute however, ordinary packet testing is highly challenged to resolve defect types, defect locations, and defect magnitudes. It is therefore a poor predictor of the interoperability of an Ethernet port under all possible link configurations and impairments.

Fully Automated Multi-Port LAN PHY Testing

The PVA-3000 introduces highly automated measurements and analyses while connected to any target 1000/100/10 BaseT LAN interface. All measurements are "live link" measurements, that is, they are performed on an active link to directly assess link partner and/or link connection performance. Measurements such as transmission power, power spectral distortion, sign-to-noise, bulk echo response, and bulk crosstalk can run on all LAN pairs and link configurations across multiple test ports without any user intervention. Link partner receiver assessments with controlled impairments such as 100M line loss, controlled transmit jitter, transmit frequency offset, and controlled ingress noise are facilitated on each test port independently with multi-port concurrent measurements of link stability and packet loss enabled. In combination with Sifos PSA-3102 test ports, DC unbalance testing from PoE is offered with full automation and reporting.

Breaking the Mold

The PVA-3000 presents an innovative, time-efficient, and highly cost-effective alternative for qualifying and characterizing 10/100/1000BaseT interfaces, providing wide-scale visibilities into performance that simply have not been available historically. Whether evaluating design components, assuring system quality, troubleshooting failures, or assessing link performance, the PVA-3000 introduces a unique new tool to enhance quality and productivity.



LAN PHY Transmission and Interface Metering with the PhyView Analyzer

The PVA-3000 introduces new measurement techniques for assessing LAN PHY Transmitter performance. These measurements enable a simple RJ-45 interface to the device-under-test and are performed on a **live link** – that is, the test instrument acts as the link partner while performing physical layer assessments. **Transmitters** and **physical interfaces** are characterized by methods that are completely independent of their receiver characteristics.

Measurement	Description	Reported Units
Rx Power Level	Single Pair (100BaseTx) or Aggregate 4-Pair (1000BaseT) RF Power at DUT Interface Rx Power Level reports aggregate transmitted power at the DUT interface. This meter adapts to an existing link rate and MDI/MDI-X configuration.	dB(nominal) Where "nominal" is the mid-level transmit amplitude specified for a 100BaseTx or 1000BaseT transmitter.
SNR	Single or Multi-Pair Signal-to-Noise Ratio SNR characterizes all forms of non-correctable signal distortion including noise or crosstalk ingress, signal compression, and severe ISI (inter-symbol interference). Meter configures desired link speed (100 or 1000BaseT) and measures specified pair. dB (Ideal Signal Power / Distortion Components) The measurement of for SNR is 36 dB.	
PSD	Single or Multi-Pair Power Spectral Distortion PSD characterizes the spectral frequency response of a LAN transmitter. PSD returns 33 evenly spaced frequency-amplitude points over a user-specified frequency range between 20KHz and 100MHz. Meter configures desired link speed (100 or 1000BaseT) and measures specified pair.	dB, Frequency Each frequency-power point is referenced to a nominal mid-level amplitude, flat frequency response 100BaseT or 1000BaseT transmitter. The measurement floor is below –30 dB.
Echo Response	Single or Multi-Pair Bulk Echo Response Bulk Echo Response is equivalent to Return Loss in a typical RF transmission system. It characterizes total reflected energy across the frequency spectrum and therefore assesses the degree of deviation from a nominal 100 transmission line. Meter measures any of the 4 pairs in a 1000BaseT link.	dB Ratio of total reflected to total transmitted power. Measurements are normalized to test port calibrations. The measurement floor is –26 dB.
Crosstalk	Single or Multi-Pair Bulk Crosstalk Bulk Crosstalk is equivalent to Isolation in a typical RF transmission system. It characterizes total power transmitted between any two specified pairs with the assumption that these transmissions are bi-directional on average. Meter measures any of the 6 pair groupings in a 1000BaseT link.	dB Ratio of total ingress (crosstalk) power to total transmitted power. Measurements are normalized to test port calibrations. The measurement floor is –32 dB.
Pair Skew	Relative Pair Timing Offset in 1000BaseT Pair Skew reports any symbol period timing differences between pairs in a 1000BaseT link. Each measurement reports 4 pairs, of which 3 pairs are each compared to a reference pair.	nsec Measurement granularity is one symbol period, or 8 nsec per pair.

PVA-3000 metering, while very different from the traditional time-domain measurements associated with LAN PHY compliance testing, is actually analogous to the types of testing typically seen in wired or wireless RF communications channels. In this regard, both measurements and calibrations are readily automated and are based upon similar conceptual constructs as seen with ordinary spectral analysis and vector network analysis. Calibrations required for PSD, Echo Response, and Crosstalk are fully automated and require no external fixturing.

PVA-3000 transmission measurements can also readily be used to assess passive link components including cabling and patch panels or to assess LAN signal integrity at any point in a LAN link.



PSD on Pairs 2 & 3 at 100BaseT

LAN PHY Receiver Testing with the PhyView Analyzer

PhyView Analyzer test ports provide configurable line impairments and metering resources to enable rapid assessment of LAN PHY receivers under **controlled stresses** either at or beyond the margins specified in the respective IEEE 802.3 specifications for 10/100/1000BaseT. Some of these impairments are also accessible to external Ethernet packet testers so that accelerated receiver assessment can be performed using traditional packet counting methods. Device-Under-Test (DUT) receiver performance is tested **fully independent** of DUT transmitter performance.

Impairment	Description	External Access
Line Emulation	Emulates IEEE 802.3 worst case line loss (attenuation over frequency). May be applied to 2 or 4 pairs such that 100BaseT transmit can be separated from 100BaseT receive pair. This impairment models 90M Cat5e + 10M Cat5e patch cable and connector losses. Maintains 100Ω line impedance and approximately linear phase characteristics.	YES
Noise (Alien Crosstalk)	Adds random noise on all pairs that is spectrally shaped similar to 100BaseT. Noise source is isolated by 2.7dB from Test PHY so that DUT experiences greater noise levels. Amplitude is programmable from –6dB to +18dB in 2dB steps where 0dB corresponds to IEEE 802.3 specified 40mVpp amplitude.	YES
Line Mismatch	Inserts a –12dB (Return Loss) Mismatch on either 2 or 4 pairs such that 100BaseT transmit can be separated from 100BaseT receive pair.	YES
Transmitter Offset	Applies a fixed frequency offset to transmitted 100BaseT and 1000BaseT signals. Frequency offset may be programmed to100ppm, -25ppm, +25 ppm, and +100ppm.	NO
Transmitter Jitter	Applies random jitter to transmitted 100BaseT and 1000BaseT signals. Jitter level may be programmed to –6 dB to +24 dB in 3dB steps where 0dB corresponds to IEEE 802.3 specified 1.4 nsec peak-peak jitter. Transmit jitter is structured to meet 1000BaseT phase noise versus frequency profile such that jitter power above 5KHz is attenuated by ~13.5 dB relative to total jitter power.	NO
Transmitter Power	Transmitter power may be controlled on 100BaseT and 1000BaseT signals over a range of ~2.1 dB (or ~25%). This range is then summed with a nominal 2.7dB fixed loss on all Test Ports. Ten power level steps are provided.	NO
Transmitter Slew	Transmitter slew rate may be controlled on 100BaseT and 1000BaseT signals over a range of 0.17V/nsec (or ~75%). Eight slew rate steps are provided.	NO

PVA-3000 metering associated with PHY receiver testing includes a configurable Link Stability meter and an MAC frame generator / counter.

Measurement	Description	Reported Units
Link Stability	Link Statistics and Sampled Link Stability Samples link status (UP or DOWN) over a configurable time duration with a configurable sampling rate. Sampling is continuous from 20msec up to 10 seconds or bursted with variable back-offs out to ~ 60 seconds.	Link "Up" Count
Packet Count	Count of Received MAC frames Each PVA-3000 port can transmit user-configured MAC frames with programmable duration, packet gap, and repeating 4-byte payload pattern. Each PVA-3000 port will count incoming MAC frames either independent of or coincident with MAC frame transmission.	Packet Count Burst transmissions of 32K, 128K, 512K, and 1024K packets are supported. Continuous transmission is also supported with counts into billions of packets.

Each PVA-3000 Test Port includes a "THRU" interface to enable LAN PHY receiver testing with external packet analyzers. This feature enables testing in situations where IP layer or higher protocols are required and/or where packet filtering must be performed as part of the packet counting process.

The PhyView Analyzer Performance Test Suite for 10/100/1000BaseT

Ethernet interface testing has never been easier than the PhyView Test Suite for the PVA-3000. This group of **fully automated tests** can be automatically sequenced across up to 24 10/100/1000BaseT ports to produce colorful and graphical reports of interface performance. Sequencing and reporting is accomplished with just a few mouse clicks in PVA Interactive software or with a single command in PowerShell PSA. Unlike traditional packet testing, PhyView Performance Tests clearly and independently characterize **transmitter**, **receiver**, and **physical interface** performance in **quantitative terms** that predict link performance over the full range of possible applications.

The LAN PHY Test Suite consists of the following tests:

PHY Test	Description	Reported Parameters
PHY Capabilities	Assesses PHY advertised capabilities for 100BaseT and 1000BaseT. Also assess auto-MDI and then verifies each of these capabilities with a link verification.	Auto-Negotiation Parameters (10BaseT, 100BaseT & 1000BaseT) Link Stability Count (10BaseT, 100BaseT, 1000BaseT, Full vs Half Duplex, MDI vs MDI-X, Master vs Slave).
100BaseT Transmission	Evaluates Transmitted Signal Characteristics of a 100BaseT transmitter both in MDI and MDI-X configurations.	Rx Power (Rx Pair 2 or 3) SNR (Pair 2 & 3) Low Frequency PSD (20KHz-2MHz, Pair 2 & 3) Wide Band PSD (4MHz – 100MHz, Pair 2 & 3)
1000BaseT Transmission	Evaluates Transmitted Signal and Interface Characteristics of a 1000BaseT transmitter.	Rx Power (Aggregate 4-Pair) SNR (Pairs 1-4) Low Frequency PSD (20KHz-2MHz, Pairs 1-4) Wide Band PSD (4MHz – 100MHz, Pairs 1-4) Time Skew (Pairs 1-4) Return Loss (Pairs 1-4) Crosstalk (Pairs 1-2, 1-3, 1-4, 2-3, 2-4, 3-4)
Line Impairment	Assesses Link Stability with IEEE 802.3 "Worst Case" Line Loss applied. Depending upon link capabilities, 10BaseT, 100BaseT MDI, 100BaseT MDI-X, 1000BaseT Moster, and 1000BaseT Slave are tested with packet traffic active. Link Stability Count Nominal Tx Power and Slew Rate Link Stability Count Low Tx Power and Slew Rate	
Noise Impairment	Assesses Link Stability with three different noise levels applied. Depending upon link capabilities, 10BaseT, 100BaseT MDI, 100BaseT MDI-X, and 1000BaseT Slave are tested with packet traffic active. Link Stability Count (+6dB Noise) Link Stability Count (+12dB Noise)	
Tx Offset Impairment	Assesses Link Stability with four different transmit frequency offsets applied. Depending upon link capabilities, 10BaseT, 100BaseT MDI, 100BaseT MDI-X, and 1000BaseT Slave are tested with packet traffic active.	Link Stability Count (-25 ppm offset) Link Stability Count (-100 ppm offset) Link Stability Count (+25 ppm offset) Link Stability Count (+100 ppm offset)
Tx Jitter Impairment	Assesses Link Stability with varying levels of transmit jitter applied. Depending upon link capabilities, 10BaseT, 100BaseT MDI, 100BaseT MDI-X, 1000BaseT MDI	
Combo Impairment	Assesses Link Stability with a combination of Line Link Stability Count	

The PhyView Test Report

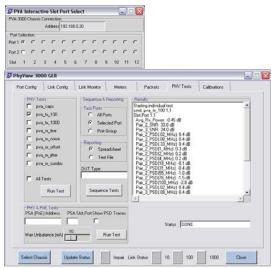
The PhyView Performance Suite produces a Microsoft Excel spreadsheet report that adds graphical presentations of test results and colorized annotations of test limit excursions. Unlike a strict compliance test, many of the parameters captured by the PhyView Performance Suite are evaluated to "soft limits" that assess performance to generally fall into one of three bands: Green (or "Excellent"), Yellow (or "Acceptible"), and Red (or "Marginal").

The report is structured such that each port tested creates a specific workbook tab dedicated to that particular port. Testing a 24 port switch would therefore cause a 24 tab workbook to automatically pop up upon completion of testing.

For those who must perform formal IEEE 802.3 physical compliance testing, PhyView Performance Reports provide an extremely efficient means to select worst-case (or bestcase) performing ports to submit to intense testing.



The Standard PhyView Spreadsheet Report



PVA Interactive Graphical User Interface

PowerShell PSA is the

Tcl/Tk-based scripting environment that has historically been available for the PowerSync Analyzer from Sifos Technologies. All testing resources and applications associated with the PhyView Analyzer are accessible via high level commands to PowerShell PSA. Customized test scripts are readily created and debugged in this interactive "live" programming environment.

PhyView Analyzer Software

PhyView Analyzer software is hosted on a Microsoft Windows (or Linux) PC and consists of two primary components that manage the instrument over a LAN interface.

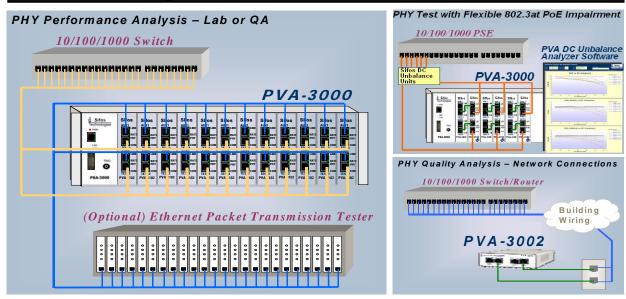
PVA Interactive is an intuitive graphical user interface that provides access to most of the features of each PhyView Analyzer test port. Using PVA Interactive, test port switching and impairments are readily configured to one or more test ports. Metering including Link Monitor, Rx Power, SNR, PSD, Echo, Crosstalk, and Skew are all configured and queried interactively. MAC frames are configured and activated as is the MAC frame receive counter. The PhyView Test Suite is fully accessible for running individual tests or sequencing

groups of tests to reports. Automated meter calibration (PSD, Echo, Crosstalk) is also readily performed from PVA Interactive.



PowerShell PSA

PVA-3000 Test Configurations



Technical Data: PVA-3000

Test Port Configurations and Measurements				
Test Category	Port Configuration	Measurements	Link Types	Calibrations
Link Partner		Power Spectral Distortion	1000 & 100Base-T	YES
Transmitter		Signal-To-Noise Ratio (SNR)	1000 & 100Base-T	NO
Measurements	Took DLIV	Bulk Echo Response	1000Base-T	YES
or	Test PHY	Bulk Crosstalk Response	1000Base-T	YES
Link Signal Quality Analyses		Pair Timing Skew	1000Base-T	NO
7 thai yooo		Receive Signal Power Level	1000 & 100Base-T	NO
Link Partner Receiver	Test PHY or Thru Port	Link Stability, 100M Cat5e	10/100/1000Base-T	Baseline
or	+ Line Emulator	Packet Loss, 100M Cat5e		Packet Loss
Outgoing Link Stability	Test PHY or Thru Port	Link Stability, Ingress Noise		(for Packet Loss Tests)
Tests	+ Alien Crosstalk	Packet Loss, Ingress Noise		L033 163(3)
	Test PHY or Thru Port	Link Stability, 12dB Mismatch		
	+ Passive Mismatch	Packet Loss, 12dB Mismatch		
	Test PHY or Thru Port	Link Stability (Multi-Impairment)		
	+ Line Emulator + Alien Crosstalk OR Passive Mismatch Test PHY +	Packet Loss (Multi-Impairment)		
		Link Stability, Jittered or Offset Xmit Signal		
	Jitter/Offset Synthesis	Packet Loss, Jittered or Offset Xmit Signal		
	Test PHY +	Link Stability (Multi-Impairment)		
	Jitter/Offset Synthesis + Line Emulator AND/OR (Alien Crosstalk OR Passive Mismatch)	Packet Loss (Multi-Impairment)		
Transmit / Receive	PVA-3102 Test Port to	(All Above Transmitter Tests)	1000 & 100Base-T	(see above)
Tests with PoE Impairments	PSA-3102 OUT Port Connection(s)	(All Above Receiver Tests and Impairments)	10/100/1000Base-T	NO
Link Partner	Test PHY	Link Partner Advertisement	10/100/1000Base-T	NO
Capabilities		Link Partner Static Responses		
		Link Partner Fallback Response		

LAN Interface Specifications				
Port	Connection Mode	Parameter	Specification	
		Connection	RJ45	
		Data Rate and Signaling	10/100/1000BaseT	
		Impedance	100Ω, Balanced	
	Terminated to Test PHY	Insertion Loss to Test PHY (1 – 100 MHz)	2.7 +0.3/-0.2 dB (All Pairs)	
		Maximum Return Loss	≤-15 dB (1 - 100MHz)	
		(All Pairs)	≤-21 dB (1 - 62.5MHz)	
Test Port	Port	Pair-Pair Isolation (1 - 100MHz)	≤ -32 dB (All Pair Combo's)	
	Terminated to Test PHY with Bulk 12dB Mismatch Connected	Return Loss (100 KHz – 100 MHz)	- 11.7 dB <u>+</u> .5 dB (All Pairs)	
		Insertion Loss (1 – 100 MHz)	2.7 +0.3/-0.2 dB (All Pairs)	
	Bypass Mode TEST Port to THRU Port	Return Loss	≤ -15 dB, 1MHz to 100MHz (All Pairs)	
	(terminated 100Ω / pair)	Pair-Pair Isolation	≤ -32 dB, 1 MHz to 100MHz (All Pair Combo's)	
		Connection	RJ45	
		Data Rate and Signaling	10/100/1000BaseT	
Auxiliary Port for Packet	Bypass Mode THRU Port to	Impedance	100Ω, Balanced	
Analyzers	TEST Port	Insertion Loss (1 – 100 MHz)	2.7 +0.3/-0.2 dB (All Pairs)	
· ······/	(terminated 100Ω / pair)	Return Loss (All Pairs)	≤ -15 dB, 1MHz to 100MHz	
		Pair-Pair Isolation (All Pair Combinations)	≤ -32 dB, 1 MHz to 100MHz	

Link Partner Transmitter / Incoming Link Integrity Measurements			
Measurement	Link Types & Pairs	Parameter	Value
		Minimum Frequency	20 KHz
		Maximum Frequency	100 MHz
	400D Tru D-in 0 0	Selectable Range	0.18 MHz – 99.98 MHz
Power Spectral Distortion	100Base-Tx: Pair 2 or 3	Frequency Resolution	Selected Range / 33 Points
	1000Base-Tx: Pairs 1 to 4	Amplitude Range	≤ -30 dB to ≥ +3 dB
		Amplitude Accuracy	<u>+</u> .5 dB (.02 – 75 MHz)
		normalized to calibration	<u>+</u> 1.5 dB (75 – 100 MHz)
	100Base-Tx: Pair 2 or 3 1000Base-Tx: Pairs 1 to 4	Measurement Range	18.5 dB – 36 dB
Signal-To-Noise		Measurement Accuracy*	<u>+</u> 1.5 dB (SNR > 30 dB)
	1000base-1x. Palls 1 to 4		<u>+</u> 0.75 dB (SNR ≤ 30 dB)
Rx Power	100Base-Tx: Pair 2 or 3	Measurement Range	≤ -15 dB to ≥ +3 dB
IX FOWEI	1000Base-Tx: Pairs 1 – 4 Aggr.	Accuracy / Repeatability	<u>+</u> 0.25 dB
Pulk Esha Pasnansa		Measurement Floor	-26 dB
Bulk Echo Response (~ 4 – 75 MHz)	1000Base-T: Pairs 1 to 4	Accuracy / Repeatability	<u>+</u> 1.0 dB @ > -20 dB
(** 4 = 75 WII 12)		normalized to calibration	<u>+</u> 1.5 dB @ <u><</u> -20 dB
Bulk Crosstalk Response	1000Base-T: Pair Combo's	Measurement Floor	-36 dB
(~ 4 – 75 MHz)		Accuracy / Repeatability	<u>+</u> 1.0 dB @ < 29 dB
(· + - 15 IVII 12)	1-2, 1-3, 1-4, 2-3, 2-4, 3-4	normalized to calibration	<u>+</u> 1.5 dB @ ≥ 29 dB
Pair Timing Skew	1000Base-T: Pairs 1 to 4	Range & Resolution	0 – 80 nsec, 8 nsec

^{* 100}BaseT and 1000BaseT SNR measurements are not referenced to any metric standards for SNR. SNR is a bulk measure of signal deviation from "ideal" following compensations for linear distortion and other correctable impairments.

Link Partner Reciever / Outgoing Link Integrity Measurements				
Measurement	Link Types & Pairs	Parameter	Value	
Link Otata and		Link States	LINKED DOWN	
Link State and	10/100/1000Base-T	Link State Samples	1 to 100	
Link Integrity		Link State Sampling Periodicity	20, 50, or 100 msec	

Link Partner Reciever / Outgoing Link Integrity Measurements				
Measurement Link Types & Pairs		Parameter	Value	
		Packet Frame	Ethernet MAC	
		Packet Size & Resolution	16 to 1512 Bytes, 4 Bytes	
		Packet Payload	Repeating 4-Byte Patterns	
Internal Took Book Books	10/100/1000Base-T	Packet Address	6-Byte Arbitrary Address	
Internal Test Port Packet Counting		Transmitted Inter-Packet Gap Time	96, 576, or 11006 nsec	
Counting		Transmitted Packet Count	0 = Continuous	
			32K, 128K, 5120K, or 1024K	
		Received Packet Count	0 to > 1e9	
		Receive Packet Filtering	NONE	
	10/400/4000	Link Partner Advertisement	Recovers 100/1000BaseT Auto-Neg Parameters	
Link Partner Capabilities	10/100/1000Base-T	Link Partner Advertisement Integrity	(Link State Measurements)	
		Link Partner Fallback Responses	(Link State & Impairments)	

Impairment Synthesis Specifications			
Impairment	Access	Parameter	Value
		Frequency Response Target	-2.1 & F ^{0.529} + 0.4/F dB
		(5 MHz – 62.5 MHz)	<u>+</u> 0.5 dB (F in MHz)
400 M (0 (5 1)		Frequency Response Target	-2.1 & F ^{0.529} + 0.4/F dB
100 Meter Cat5e Line Emulation	TEST Port & THRU Port	(62.5 MHz – 100 MHz)	<u>+</u> 1.5 dB (F in MHz)
Lindiation		Return Loss (1 – 100 MHz)	<u><</u> -14 dB
		Isolation (1 – 100 MHz)	(not yet specified)
		Phase Linearity (4 – 100 MHz)	+ 12° from Linear (typical)
	TEST Port connected to	Fixed Return Loss	- 11.7 dB <u>+</u> .5 dB
Passive Mismatch	Test PHY or THRU Port	(TEST Port, 1 – 100MHz)	
	Test PHT OF THRO POIL	Impairment Application	Pairs 1+3, 2+4, or 1+2+3+4
	TEST Port connected to Test PHY or THRU Port	Amplitude Range	-6 dB - +18 dB
		relative to 40mVpp (= 0dB)	
Alien Crosstalk		Amplitude Steps (Resolution)	2 dB
		Frequency Shaping	100BaseTx Spectrum
		Impairment Application	Pairs 1+3, 2+4, or 1+2+3+4
	Test Port with Test PHY Connected	Nominal Transmit Frequency	125 MHz <u>+</u> 50 ppm
Transmit Frequency Offset		Nominal Transmit Duty Cycle	50% <u>+</u> 12.5 %
		Fixed Frequency Offsets	-100, -25, 0, +25, +100 ppm
		Jitter Magnitude Range	-6 dB to +24 dB
	Test Port with	relative to 1.4 nsec pk-pk (=0dB)	
Transmit Frequency Jitter	Test Port with Test PHY Connected	Jitter Level Steps (Resolution)	3 dB
	rest PHY Connected	Jitter Power @ greater than	-13.5 dB <u>+</u> 1 dB
		+ 5 KHz w.r.t. Total Jitter Power	
	Test Port with	Mid-Range Output Power	-2.7dB (IEEE Spec.)
Transmit Power	Test Port with Test PHY Connected	10/100/1000BaseT Peak Ampl.	at setting = 4 out of 10
	rest ant Connected	Transmit Power Range	-4 dB to -1.9 dB (IEEE Spec)
	Toot Port with	Mid-Range Slew Rate	~0.2V/nsec
Transmit Slew	Test Port with Test PHY Connected		at setting = 5 out of 8
		Slew Rate Range	~0.1V/nsec to ~0.27 V/nsec

LED Indicators			
LED Label	Parameter	Description	
Rate	Link Indication	ON: 1000BaseT, BLINKING: 100BaseT, OFF: 10BaseT	
Link	Link Status	ON: Link Up, OFF: Link Down	
Impr	Line Impairment Connection	ON: 100M Cat5e Line Impairment Connected OFF: Line Impairment Removed	
Com	Communications	ON or BLINKING : Indicates Host Communications to PVA-3102 Test Port	

Programming and Control	
Description	Specification
Interface	Ethernet 10/100BaseT
Host Requirements	PC running Microsoft Windows 2000, XP, Vista, Win7, or Linux PC (Fedora, SUSE)
Control Environment	Sifos PowerShell or PSA-Interactive
Recommended Network Latency:	< 10 msec

Physical and Environmental	
Description	Specification
Dimensions	19"W x 5.25"H x 12"L (3U Rack Mount)
Weight	20 lbs. (Fully Populated with PVA-3102 Cards)
Power	100VAC-240VAC, 50-60 Hz, 1350mA Max.
Ambient Operating Temperature	0°C to 50°C (≤ 42.75 Watt loading per port)
Storage Temperature	-20°C to 85°C
Operating Humidity	5% to 95% RH, Non-Condensing.

Certifications	
Description	Certifications
Emissions	TBD –Testing Initiated,
	PSA-3000 Chassis: FCC Part 15, Class A; EN55022; VCCI, AS/NZS 3548
Safety	TBD – CSA Testing Planned
	PSA-3000 Chassis: CSA Listed, EN61010-1, CB Scheme IEC 61010-1
European Commission	TBD – CE Testing Planned
	PSA-3000 Chassis: 73/23/EEC, 89/336/EEC, CE Marking Directive 93/68/EEC
FCC Statement: (Testing for Class A digital device Planned)	

Ordering Information

PSA-3000	PowerSync Analyzer 3000 Chassis & Controller, PowerShell PSA, and PSA Interactive Software
PVA-3102	Dual Port PhyView Analyzer Test Card for PSA-3000 (Maximum 12 per Chassis)
PVA-3002	Compact 2-Port PhyView Analyzer
PVA-PTS	PhyView Performance Test Suite for Single PhyView Chassis (PSA-3000, PVA-3002, PSA-1200)
PVA-LIU	In-Line Passive Test Impairment Unit (4 Fixed Impairments)
PVA-DCU	In-Line DC Unbalance Generator Unit (ALT A/B, Forward and Reverse Channels)

Accessories Included:

- Installation Guide & Configuration Chart
- PhyView Analyzer Reference Manual (Binder and CD)
- High Performance Test Cables (1 cable per Test Port)
- Power Cord
- Cross-Over Ethernet Cable
- RS-232 Cable

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