

Update: April, 2018

Sifos Technologies test equipment may now be utilized to perform Power Sourcing Equipment (PSE) and Powered Device (PD) testing required for **Ethernet Alliance** (EA) POE Certification. More specifically, the **PSA-3000** PowerSync Analyzer may be utilized for certification testing of 802.3at compliant PSE's and the **PDA-602** Powered Device Analyzer may be utilized for certification testing of 802.3at compliant PD's.



In order to utilize Sifos test equipment for EA certification testing, product designers and manufacturers must first

obtain certification from the Ethernet Alliance to operate as an **Authorized Testing Lab** with the purpose to perform **1**st **Party** testing. There are *clear benefits* to pursuing this certification that will be explained below.

Why Obtain EA PoE Certification

Obtaining EA PoE Certification for a PSE or a PD allows network equipment and device manufacturers to apply an industry recognized **logo**, or certification mark, on their certified products and within associated product literature for those products. The EA logo communicates that the marked product is designed to meet all of the technical requirements essential for safety and interoperability according to the **IEEE 802.3at** industry specification for PSE's and PD's. Customers who deploy PoE in their networks will gain greater confidence that the PoE products they are purchasing are safe, will interoperate correctly, and will not damage non-PoE equipment.

Further, the EA PoE logo clearly and simply communicates a power category for each certified item. This enables users to readily understand if there is a possible mismatch between the power available from a PSE port and the power required by a PD. This feature will especially be vital as the next generation of PoE, **IEEE 802.3bt**, deploys in the coming years.

Information Resources

Webinar	Introducing the Ethernet Alliance Power over Ethernet Certification	Register at: http://bit.ly/EA-PoECertWebinar				
	Tuesday, May 1, 2018 10AM Pacific Daylight Time					
Home Page & Program Overview	Ethernet Alliance PoE Certification	Visit: https://ethernetalliance.org/poecert/				
Certified Product Registry	The EA maintains a publicly accessible registry of EA certified products including test equipment that may be used by Authorized Test Labs	Visit: <u>https://ea-poe-cert.iol.unh.edu/</u>				
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Why Become a 1st Party Authorized Test Lab

The EA PoE Certification Test Program offers two alternatives by which products are tested for EA PoE certification:

3rd Party: Product is submitted to an independent Authorized 3rd Party Test Lab for testing. UNH-IOL is the only 3rd Party Test Lab for EA Certification testing at this time.

1st **Party**: Product is tested in-house using certified test systems. EA Certification reports are produced and submitted to the Contracted Auditor. UNH-IOL is also the Contracted Auditor.

For producers of PoE equipment, the **1**st **Party** alternative may offer *significant* benefits.

- Lower external costs to obtain and maintain EA logo certifications across a range of products (*See* Setting Up An EA Authorized 1st Party Lab *below*.)
- **Reduced engineering effort** as testing normally performed to verify designs and design changes can also support the certification
- Faster turn-around time to obtain and update EA logo certifications
- **Flexibility** to routinely update products and to demonstrate certification compliance with minimal incremental effort
- Avoid effort and information disclosures required to justify multiple product versions (also referred to as "derivative product")
- **Overcome technical limitations** of a 3rd Party lab where "worst case" test conditions may be difficult or impossible to produce (e.g. PD's operating a maximum power draw)

Best of all, many *potential* 1st Party test labs *already own* and are *routinely using* Sifos test instruments and software that are presently qualified to meet the requirements for EA logo certification testing. *(See Sifos Test Equipment Requirements below.)*

Setting Up An EA Authorized 1st Party Lab

To become an Authorized 1st Party Lab, a PSE or PD supplier must fulfill the following requirements:

	Provider	Fee Type	Fee Amount	
Join Ethernet Alliance	EA	Annual PoE Certification	\$ 3,000*	
		Membership		
Obtain CMLA License from EA	EA	One Time	\$ 5 <i>,</i> 000**	
Apply for Device Certification	EA	One Time per Product	\$ 1,000**	
Obtain 1 st Party Lab Certification	UNH-IOL	Annual	\$ 8,999	
from UNH-IOL				
Apply for Derivative Product	EA	One Time per Derivative	\$ 100	
Certification		Product		

** Higher level EA memberships are available at additional annual cost.

* Discounts may be available given EA special promotions or member-specific circumstances. Contact the Ethernet Alliance for further information.



Authorized 1st party labs are required to be EA members. EA memberships exist at various levels for various fees. The minimum required membership level in this case is \$3000 for a PoE-only membership. One important benefit of the membership is that it reduces cost of CMLA license and each of the application fees for new product and derivative product.

The CMLA license is the legal agreement that defines rules for using the EA PoE logo including how is used, where it is used, and so on. The EA protects against any fraudulent usage of the EA PoE logo through licensing agreements and trademarks.

UNH-IOL is the Authorized 3rd Party Test Lab and the EA Contracted Auditor for the EA PoE logo certification program. In this role they perform testing and auditing to:

- 1) Certify individual products (PSE's and PD's) for EA PoE logos
- 2) Certify test equipment and methods that 1st party test labs can deploy
- 3) Qualify 1st party test labs and review test data produced by 1st party test labs
- 4) Publish and maintain registry of certified PSE's and PD's
- 5) Publish and maintain registry of certified test equipment

An EA Authorized 1st Party Test Lab pays an annual membership fee to the Contracted Auditor, UNH-IOL, for this service. Each 1st party test lab must operate from a single location or facility.

Finally, there are one-time application fees for each new product and each derivative product seeking certification. Applications are submitted to the Ethernet Alliance.

Example: Certify 5 new PSE's and 5 new PD's for EA logos in one year assuming CMLA license executed previously.

EA Membership Fee	\$ 3,000	
UNH-IOL Fee	\$ 8,999	
Product Application Fees	\$ 10,000	(EA member rate)
Total	\$ 21,999	(\$ 2,200 per product)

By comparison, if the equivalent number of product certifications were performed via 3rd party testing by UNH-IOL for a Non-EA member who had previously obtained the CMLA license:

PSE Testing at UNH-IOL	\$ 24,999	(5 x \$ 4,999)
PD Testing at UNH-IOL	\$ 14,999	(5 x \$ 2,999)
Product Application Fees	\$ 25,000	(10 x \$2,500)
Total	\$ 64,998	(\$ 6,500 per product)

Authorized 1st Party Test Labs obtain certifications by submitting test data (or sanctioned test reports) to UNH-IOL rather than submitting product. Furthermore, Authorized 1st Party Test Labs can similarly obtain certifications for derivative product, that is modifications to previously certified products by submitting updated test data (or sanctioned test reports) to UNH-IOL. This avoids the need to document specifics about each product variant or modification.



Sifos Test Equipment Requirements

Sifos has applied for and successfully received certification for test equipment to certify both PSE's and PD's for the EA POE Certification Program. This means that Sifos customers who presently utilize this test equipment may apply to become 1st Party Authorized Test Labs. Existing customers can do so without any further investment is Sifos test equipment.

Much like certified PSE's and PD's, test equipment and associated software is certified by hardware, firmware, and software versions. The following table includes Sifos products and versions that have been certified for use by Authorized 1st Party Test Labs.

Application	Hardware	Firmware	Host Software		
PSE Testing	PSA-3000 including:	Controller: 3.14	4.2.5		
	PSA-3202 version 8				
		4.0c, ALC Firmware v13	(Includes		
	PSA-3000 including:	PSE Conformance			
	PSA-3102 version 8 and/or	Test Suite 4.2.13)			
	PSA-3102 version 6 and/or				
	PSA-3102 version 5 and/or				
	PSA-3102 version 4 and/or				
	PSA-3102 version 3 and/or				
	PSA-3102 version 1				
PD Testing	PDA-602B	2.0	1.10		
	PDA-602B	1.7			

To pursue EA certification for PSE's and PD's, testing is performed by sequencing the respective Conformance Test Suite (PSE or PD) using the optional selection for "**EA Cert. Test**". This option is available under the respective Conformance Test menus and also may be specified using command lines that sequence conformance tests.

PowerSync Interact	ive - Test Sequencer			A PDA-600 Interactive	
PSE TYPE & 30W Grant C NONE C NONE C LUP F Include LUP PD Emulation C 15.4 W C 30.0 W MPS Method C DC C AZ	PSE 802.3 Conformance T Conformance Tests Ver. 4.216 det_v det_i det_ange det_ime class_uime det_resource class_time prurup_ime class_time prurup_ime class_time prurup_ime class_time prurup_ime promor_ownait mps_ac_wrift pruron_overid mps_ac_vrift prurdn_vime mps_ac_wrift prurdn_vime mps_ac_wrift	Slots Ports 1, 0 1 0 2 2, 0 1 0 2 3, 0 1 0 2 4, 0 1 0 2 5, 1 1 2 2 7, 1 1 2 2 8, 1 1 2 2 9, 1 1 2 1 10, 1 1 2 1 11, 1 1 2 1 1 2, 1 1 2 1 1	Test Reporting Test Dycles: 1	File Help Analyze 802.34 CT LANALLOP Passive Tests Passive Tests	ed Tests II Quadrants F Type 1 Phy LT A, MDI F Type 2 Phy LT A, MNIX F Type 2 LDP LT B, MDIX Lawel (DCV) Dwell Time (sec) 1 [42:0] Powered Phy 2 [54:0] Powered Phy Uncovered (sec) [10] S transpt before start Dwell Time powered test
CHECK CONNECTS	sequence tests	SA Inte	ractive	I View Re ✓ EA Cert. Test	start Text Stop Text port

EA Certification Option in **PDA Interactive**



When the **EA Cert. Test** option is selected, the available tests will be fixed and, upon completion of the automated testing, a special **EA Test Report** will be produced. The EA Test Reports are locked Excel spreadsheets that cannot be edited or modified.

EA PoE Certification Tes	t - PSE						d cif	••*		802.3at 0	Conform	ance Repo	ort				
February 17 2018 5:41 PM Port Count 4							Techno	loaies	Test Mode	30 Watt I	РНҮ	version report version	4.2.13				
Loop Count	1						1001110	EA C	ertification:	Pa	cc	report versic	11 4.2.04				
PSE Tested: Sample Type-2 PSE							Error Log:	None	1	ı a	33						
Chassis ID: 192.168.221.84	EA Teat ID	2-2	PSA-300	0 Ports	8-2		Min	Max	Avorago	Low	P/F	High	P/F				
Test: det_v	Test ID	2-2	4-2	0-2	0-2	UNITS	WIIII	WidX	Average	Linin		Linit					
Open_Circuit_Det_Voc=	1.2	5.97	5.83	6.15	5.78	volts	5.78	6.15	5.93	3 2.	8 Pass	30	Pass				
Min_Det_Vvalid=	1.3	5.21	5.21	5.24	5.19	volts	5.19	5.24	5.2	2 2.	.8 Pass	9	Pass				
Det_Volt_Step_dVtest=	1.3	1.64	1.63	1.67	1.68	volts	1.63	1.68	3 1.7	·	1 Pass	7.2	Pass				
Good Sig Det Pulse=	1.3	0	1	1	1	v/usec edges	0	1			0 Pass 1 Pass	0.1	Pass				
Backoff_Voltage=	1.9	0.1	0.1	0.1	0.1	volts	0.1	0.1	0.1		0 Pass	2.8	Pass				
Non_802_Step_V=		2.13 9.35	9.36	2.17 9.39	9.34	volts	2.13	2.17	2.2	3	8	0.1					
Non_802_Discr_?=		0.00	0.00	0.00	0.01 C	****	0.01	() ()	0	0					
Detect_Strategy=		1	1	1	1	••••	1	1	1		0	2					
Rgood_Max=	1.4	27	27	27	28	Kohm	27	28	3 27.3	3 2	Pass	32	Pass				
Rgood_Min=	1.4	16	16	16	18	Kohm	16	18	3 16.5	1	6 Pass	19	Pass				
Rmid_det= Cqood Max=	1.8	0.1	0.1	0.1	28	uF	0.1	28	0.1	2	0 Pass	33	Pass				
Rbad_Cbad_Stat=		0	0	0	C	••••	0	() (0	0	0					
Test: det_time Backoff Time Tdbo=	1.9	2656	2637	2656	2637	msec	2637	2656	2646.5		-1 Pass	1500	Info				
Eff_Backoff_Tdbo_eff=	1.0	2656	2637	2656	2637	msec	2637	2656	2646.5		-1	1500					
Backoff_Type=	1.8	0	0	0	66	****	0	(0 (0 5 Pass	0 500	Pase				
Total_Det_Time=	2.14	145	145	145	145	msec	145	145	5 145	5	5 Pass	1000	Pass	FΔ	Toct	Ronnr	
Test: class_v	4.5	47.7	47.7	47.0	_									L7	1030	перы	
Class_Voltage_Vclass= Vclass Min=	1.5	17.7	17.7	17.8	EA	PoE Cer	tificatio	n Test	- PD	c	Coverage:	ALT A	MDI	Type-1	EA Certification	À e	
Mark_Voltage_Vmark=	1.5	8.5	8.5	8.7	-	and Testad	4	/16/2018	4:19 PM			ALT B	MD1-X	Type-2 PHY	Pass		SITOS
Mark_Voltage_Min=	1.5	8.5	8.5	8.4	Proc	UCT lested:				C	Color Key:	EAU	MARN	Type-2 LLDP	Casial Number	Tecr	inologies
Event_Count=	1.7	2	2	2	Det	ection & Cl	assificatio	n		PSE E	PASS mulation	Pairs		Polarity:	MDI	60480018	(pt ver: 1.1
Event1_Tcle1=	1.6	23.3	23.4	23.4	_	Parameter	acomouno	 	A Test ID	Meas.	Units	Min.	Max.	Average	Low Lim.	High Lim.	P/F
KVent2_TCTe2= Mark_Tme1=	1.6	7.8	5.8	24.2	-	Rdet			1.2, 1.3	24.97	kohm	24.97	24.9	7 24.97	23.70	26.30	P
Mark_Tme2=	1.6	169.2	249.2	89.6		Rdet_unpwr Cdet			1.4	>99.00	uF	99.00	99.0	9 0.09	<12.00	>45.00	P
Test: class_err Class lim=	1.5	76	77	77	-	1 Event Class	sification										
Pwr_Cl_lim=	1.5	0	0	0		Iclass CloceNum			1.2, 2.11	39.8	mA	39.8	39.	8 39.8	36.0	44.0	P
Pwr_C1_55=	1.5	0	0	0	-	Tclass			2.13	0.0005	sec	0.0005	0.000	5 0.0005	0.0005	0.0050	P
Pwr_Cl_Uneven=	1.0	0	0	0		ClassStabilit	y		2.13	1					1	1	Р
Treset=		398	398	398	-	2 Event Class Iclass_event?	sification		1.2. 2.12	39.8	mA	39.8	39	8 39.8	36.0	44.0	P
Pwr-On_Rise_Time_Trise=		58	99	93	-	Iclass_event2	2		1.2, 2.12	39.8	mA	39.8	39.	8 39.8	36.0	44.0	P
Power-On_Time_Tpon=	2.13	224.6	304.7	144.5		Markl ClassNum2			2.12	0.99	mA	0.99	0.9	9 0.99	0.25	4.00	P
Test: pwrup_inrush Init Iinrush=		436.25	438	442	-	Tclass_event	1		2.12	0.0005	sec	0.0005	0.000	5 0.0005	0.0005	0.0050	P
Max_Iinrush_c4=	3.17	437.63	439.25	443.63		Tclass_event	2		2.13	0.0005	sec	0.0005	0.000	5 0.0005	0.0005	0.0050	P
Min_Iinrush=	3.17	435.25	436.5	441 63.9	-	ClassStabilit	y_event1 v event2		2.13	1					1	1	P
Inrush_45m=	0.11	53.6	53.6	53.6	Pov	/er-Up / Do	wn										
Inrush_Voltage=		31.5	32.1	32.1	-	Parameter			A Test ID	Meas.	Units	Min.	Max.	Average	Low Lim.	High Lim.	P/F
Inrush_Strategy_c4=		430.8	437	0	-	Inrushi_1			1.8	216.9	mA	216.9	216.	9 216.9 0 191.0	0.0	400.0	P
Test: pwron_v	2.44	50.7	50.0	50.0		Pmax_Tdelay	y		1.8	4.7	W	4.7	4.	7 4.7	0.0	14.4	Р
Vport_min_2= Vport_max 2=	2.11 2.11	52.7	52.8	52.8 53.8	-	Von			1.6	37.9	VDC	37.9	37.	9 <u>37.9</u> 5 <u>33.5</u>	30.0	42.0	P
Vport_ripple_2=	2.10	17	17	18		BackfeedV			1.1	0.1	VDC	0.1	0.	1 0.1	0.0	2.8	P
Vport_noise_2= Vtrans_min_2=	2.10	125 52.5	122 52.5	122 52.6	MD	Powered 1	Гуре-1			PSE E	mulation	On Time:	300 sec	Off Time:	10 sec	Vport:	48.0
Vtrans_max_2=	2.11	53.9	53.9	54		Parameter Pneak 1			A Test ID	Meas.	Units	Min. 7.09	Max.	Average 7 09	Low Lim.	High Lim.	P/F
Test: pwron_pwrcap		22.6	22.6	22.7	-	Pavg_1			1.5, 2.10	6.47	Ŵ	6.47	6.4	7 6.47	0.0	13.0	P
Icon_%_c4=	3.15	32.0	32.0	111.3		MPSViolation	n_1 /iolation_1		1.7	0		0		0 -	0	0	P
Type-2_Enable=	1.7	1	1	1	-	DutyCycleVi	plation_1		1.9	0		0		0-	0	0	P
Ilim_Peak=		5.3	0.8	1.8	MD	Powered 1	Type-2 PH	Y		PSE E	mulation	On Time:	300 sec	Off Time:	10 sec	Vport:	54.0
Ilim_Min_2=	3.16	685	684.7	686.7		Parameter Ppeak 2		-	A Test ID	Meas.	Units	Min.	Max.	Average	Low Lim.	High Lim.	P/F
Tlim_2= Vlim 2=	3.15	52.4	52.9	64.1 52.4		Pavg_2			1.5, 2.10	6.53	W	6.53	6.5	3 6.53	0.0	25.5	P
Ilim_Max_2=	3.15	836	830.5	835.8		MPSViolation	n_2		1.7	0		0		0 -	0	0	P
Ilim_Low_V_Tol_2= Ktran_lo_2=	2.12	63.3 104.3	63.3 104.5	61.7 104.5	-	DutyCycleVi	plation_2		1.9	0		0		0-	0	0	P
Test: pwron_overld					MD	Powered 1	ype-2 LLC)P		PSE E	mulation	On Time:	300 sec	Off Time:	10 sec	Vport:	54.0
%Ipeak_2=	3.16	125	125	125	- 1	Parameter			A Test ID	Meas.	Units	Min.	Max.	Average	Low Lim.	High Lim.	P/F
Vport_S%DC_2=	2.12	52.4	52.5	52.5	-	Ppeak_PreAllo			1.9, 2.10	6.50	w	7.31	7.3	1 7.31	0.0	14.4	P
Test: mps_dc_valid	0.00	50	50	50		MPSViolation	n_PreAlloc		1.7	0		0		0 -	0	0	P
Min_valid_Time_Tmps= Duty Cycle tol=	3.29	50	50	50	-	TcutWindow	violation_Pre	Alloc	1.9	0	_	0		0-	0	0	P
Test: mps_dc_pwzdn	0.42					Ppeak_Post/	Alloc		1.9	7.09	W	7.09	7.0	9 7.09	0.0	12.4	P
Min_Valid_I_hold= Time-to-Shutdown_Tmpdo=	3.19	341	350	333	-	Pavg_PostAl	loc		1.5	6.27	W	6.27	6.2	7 6.27	0.0	11.2	P
Max_Voltage_Vopen_max=		0.1	0.2	0.1		TcutWindow	PostAlloc /iolation PostAlloc	stAlloc	1.7	0		0		0 -	0	0	P
Test: pwrdn_time	3.23	Q E	87	0.1		DutyCycleVi	plation_Post	Alloc	1.9	0		C		0 -	0	0	Р
Output_Cap_Cout=	3.23	0.0467	0.0546	0.0598	PD	LLDP Proto	col Chara	cteristics	5		Links				1 F.	10.06.04	D/F
Output_Load_Rp=		79.2	67.8	63.8	-	Parameter FirstRegTime)		A Test ID 2.10,	Meas. 43.0	Sec	Min. 43.0	Max. 43.	Average 0 43.0	Low Lim.	High Lim.	INFO
Test Port Hardware Version:		3202	8	5102		PowerReque	st		3.14,3.15,	11.2	W	11.2	11.	2 11.2	0.1	25.5	Р
Test Port Firmware Version:		4.0c	3.23	3.24		PDAckTime AllocPowerF	choed		3.16,3.17, 3.18	2.0	sec W	2.0	2.	0 2.0 2 11.2	0.0	10.0	P
FA Test Renort	- PSF					PDA Software	version		1.10	(1.4			. II.	- 11.2	11.2	11.2	
		•				PDA Firmwar	e version		2.00								